



520.37857CV3

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: K. KANETANI, et al.
Application No.: 10/754,596
Filed: January 12, 2004
For: SEMICONDUCTOR INTEGRATED CIRCUIT AND
SEMICONDUCTOR LOGIC CIRCUIT USED IN
THE INTEGRATED CIRCUIT
Art Unit: Unknown
Examiner: Unknown

SUPPLEMENTAL PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

March 9, 2004

Sir:

In supplement to the Preliminary Amendment filed January 12, 2004, please amend the above-identified application as listed below and as set forth on the following pages:

Amendments to the Abstract;

Amendments to the Claims;

Remarks are included following the amendments; and

APPENDIX A including a new Abstract is attached following the remarks.